

Exceptional service in the national interest



Multi-Project Wafer Program

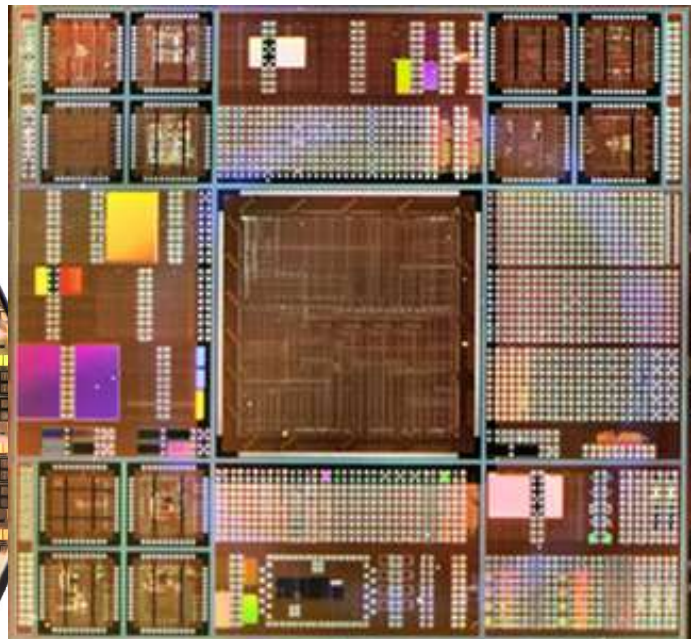
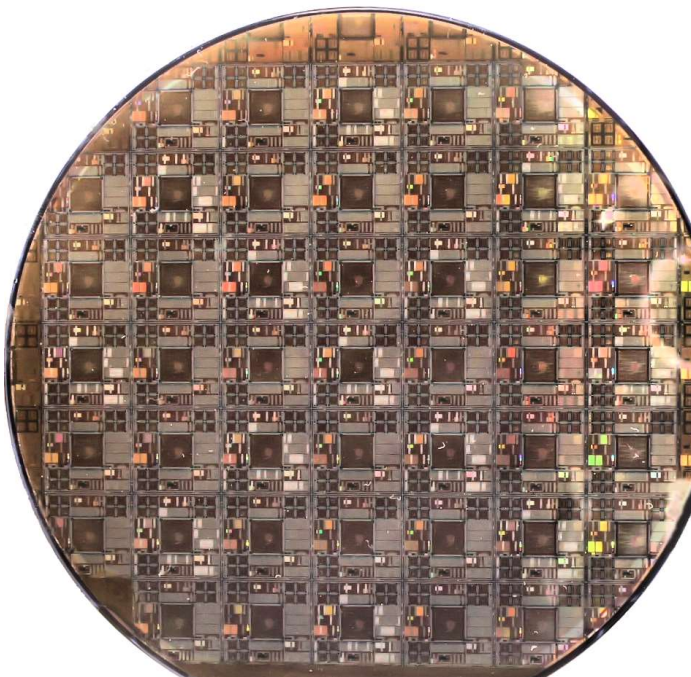
Sandia National Laboratories' Multi-Project Wafer (MPW) Program provides MPW services for CMOS7 radiation-hardened Application-Specific Integrated Circuits (ASICs).

MPW services integrate several different integrated circuit designs on a single reticle set. Sharing mask and wafer resources reduces the overall cost per design, making it more cost effective to produce integrated circuits in low quantities.

Sandia's MPW Program provides access to its Trusted Design Center and high reliability ASIC production facility, which is governed by the Sandia Quality Management System that is certified to the ISO 9001:2015 standard. The program includes engineering support for MPW design, technology, fabrication, test and packaging.

Program features include:

- 3.3V, 350nm, mixed-signal, CMOS SOI technology
- Total Ionizing Dose (TID) and strategically radiation hardened by process
- Resistors, MIM capacitors, and diode passives
- Standard Cells and IOs
- Library of common digital and analog elements
- Cadence Process-Design Kit



MICROSYSTEMS, ENGINEERING, SCIENCE & APPLICATIONS

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