Characterization of a Surface Micromachined Pressure Sensor Array

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ABSTRACT

A surface micromachined pressure sensor array has been designed and fabricated. The sensors are based upon deformable, silicon nitride diaphragms with polysilicon piezoresistors. Absolute pressure is detected by virtue of reference pressure cavities underneath the diaphragms. For this type of sensor, design tradeoffs must be made among allowable diaphragm deflection, diaphragm size, and desirable pressure ranges. Several fabrication issues were observed and addressed. Offset voltage, sensitivity, and nonlinearity of 100 μm diameter sensors were measured.

1.   INTRODUCTION

Recently, there has been a great deal of interest in micromachined sensors and actuators. The Microelectronics Development Laboratory (MDL) at Sandia National Laboratories has developed a surface micromachined pressure sensor which complements other projects and is intended to be used as a modular building block for microsystems applications. In addition, there are other specific industrial and defense applications for this sensor. All materials and processes used for sensor fabrication are CMOS compatible, and are part of the ongoing effort of CMOS integration with MEMS at the MDL[1].

While the principal objective of the project was to develop a robust, wide-range pressure sensor technology, many specific goals were put forth. The required pressure range of the sensor array is 1-10 atm (absolute), with frequency responses from DC to 2 MHz. Most of the processing equipment used for micromechanics at the MDL is shared with a 0.5 μm CMOS line. The requirement for 100% CMOS compatibility was to guarantee that sensor lots would not contaminate equipment, and also to allow integration with CMOS circuitry if necessary. Conceivable electronic functions are: signal conditioning, calibration, temperature compensation, analog to digital conversion, or network communication protocol. Finally, the desired operating environments were gaseous (including air), liquid, and severe (e.g. corrosive). Operation in all of these media provides enhanced flexibility of applications.

2.   EXPERIMENTAL

2.1   Sensor Design

The two principal design considerations for a diaphragm-based pressure sensor are diaphragm geometry and piezoresistor size and placement. These piezoresistors act as strain gauges that change their resistance value according to the deflected state of the diaphragm. This sensor design utilizes both circumferentially and radially oriented piezoresistors, which have opposite resistance changes. This feature can be used to increase the overall sensitivity of the device. Details of piezoresistor size and placement are described elsewhere[2,3]

The geometry of the diaphragm effects the mechanical deflection under applied pressure and, hence, the sensitivity and range of the sensor. A circular geometry was chosen for the pressure sensor. Circular symmetry lends itself to analytical solutions of plate theory. The deflection at the center of an edge-clamped plate due to a uniform applied pressure is given by

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Timoshenko[4] as:

$$w = \frac{Pa^4}{64D}, \qquad D = \frac{Eh^3}{12(1-n^2)} \tag{1}$$

and the corresponding radial and circumferential strains are:

$$e_r = -\frac{3Pa^2(1-n^2)}{8Eh^2}\left[1-3\left(\frac{r}{a}\right)^2\right]$$

$$e_q = -\frac{3Pa^2(1-n^2)}{8Eh^2}\left[1-\left(\frac{r}{a}\right)^2\right] \tag{2}$$

where

$a \equiv$ diaphragm radius [m]	$D \equiv$ flexural rigidity [N • m] or [Joule]	$E \equiv$ Young's modulus [N / m²] or [Pa]
$e_r, e_q \equiv$ radial, circumferential strain [unitless]	$h \equiv$ diaphragm thickness [m]	$n \equiv$ Poisson's ratio [unitless]
$P \equiv$ applied pressure [N / m²] or [Pa]	$w \equiv$ diaphragm deflection [m]	$r \equiv$ radial coordinate [m] (zero at center)

Materials constants for silicon nitride are given in the caption of Table 1. Equations (1) and (2) describe deflections that are proportional to (a^4/h^3), but strains (and piezoresistive sensitivities) that are proportional to (a^2/h^2). For the pressure sensor presented here, the allowed deflection is controlled by the thickness of the sacrificial oxide stack. Thus, design tradeoffs must be made among allowable deflection, diaphragm size, and desirable pressure ranges. A thorough description of a design process of a similar sensor is presented by Burns[3]

Seven sizes of diaphragms were designed. Resonant frequencies and maximum applied pressure before full deflection were approximated by using simple plate theory and are shown in Table 1. By using an array of diaphragm sizes, a wide range of pressures can be accurately sensed. Table 1 suggests that the larger sensors should be fully deflected at atmospheric pressure. These sensors may find use as vacuum sensors and in other low pressure applications. Overpressure bursting of large membranes while operating in the pressure regime of the small membranes is not expected to be an issue since the diaphragm travel will be stopped by the underlying substrate. Excellent overpressure behavior of similar surface micromachined pressure sensors has been demonstrated by other researchers[5].

h [μm]	a [μm]	f_0[MHz]	P_{max} [psia]
0.8	25	7.0	650
0.8	50	1.8	41
0.8	75	0.78	8.0
0.8	100	0.44	2.5
0.8	125	0.28	1.0
0.8	250	0.070	0.06
0.8	500	0.018	0.004

Table 1. Theoretical diaphragm characteristics with varied diaphragm radius. Materials constants for Si_3N_4: E=300[GPa], ρ= 2.3 [g/m³], ν= .25

A schematic representation of the pressure sensor is shown in Figure 1. A circular silicon nitride diaphragm forms the top of a sealed vacuum cavity, which provides a reference pressure. Polysilicon strain gauges are mounted on top of the diaphragm. As the membrane deflects due to an applied pressure, the edges of the diaphragm are placed in radial tensile strain, while the center is placed in circumferential compressive strain. For properly designed resistors, this behavior causes resistance changes in the radial and circumferential strain gauges that are equal but opposite in sign. To first order, the resistance changes are directly proportional to the applied pressure.
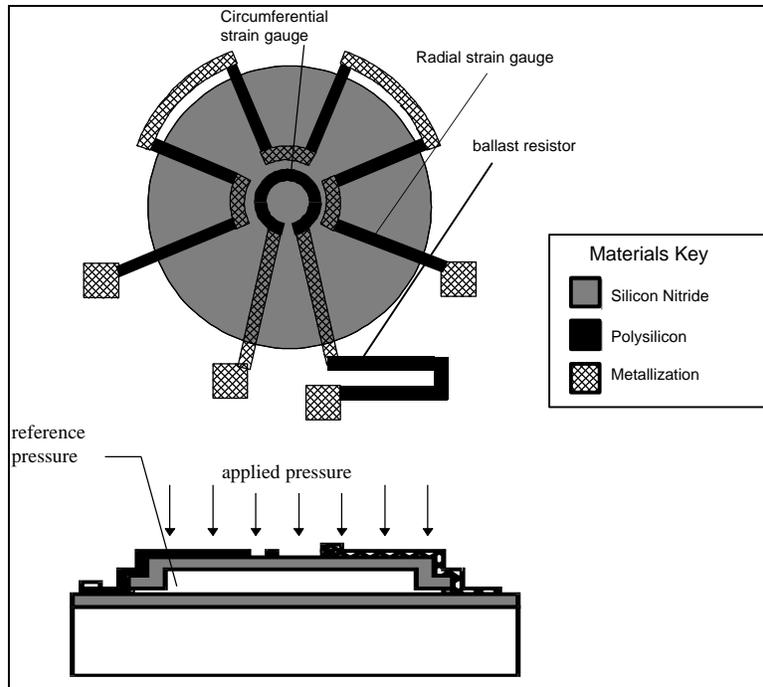
Figure 1. Schematic of pressure sensor.

2.2   Fabrication Sequence

The fabrication sequence of the sensor is shown schematically in Figure 2 and in optical and electron micrographs in Figure 3. The first step is to deposit a 0.8μm thick base layer of low stress silicon nitride film (Figure 2a). This acts as an insulating base layer for resistor and metal lines, and as a non-stick layer so that a fully deflected membrane is less likely to adhere to the substrate. The nitride is deposited in a VTR vertical low pressure chemical vapor deposition (LPCVD) furnace and the resulting film thicknesses generally vary from the top to the bottom of the boat and radially accross a single wafer. This is due to diffusion limitations during deposition. Thickness variations are not an issue for the base layer, but become more important for the diaphragm layer.

Following the base layer nitride deposition an LPCVD oxide based on tetra-ethyl-orthosilicate (TEOS) is deposited. The deposition is 2 μm thick and is performed in a VTR vertical tube furnace. The resultant film is patterned as shown in Figure 2(b) and Figure 3(a). Then, a thinner 0.1μm layer of LPCVD boro-phospho-silicate glass (BPSG) is deposited and patterned (Figure 2(c)). This glass is also TEOS based, but is doped with 5% boron and 5% phosphorous and is deposited in a LAM Integrity reactor. The combined thickness of the thin and thick oxides determine the maximum diaphragm deflection. The composite film of TEOS and BPSG is used to achieve a faster release etch rate than would be attainable by TEOS alone.

After the oxide deposition steps, another 0.8μm film of low-stress silicon nitride is deposited and patterned (Figure 2d). This layer of nitride is the diaphragm material and is anchored to the base layer of nitride through cuts in the sacrificial oxide layers. During the dry etch of the nitride, the sidewalls of the photoresist are preferentially etched. This causes failure of the photoresist and local thinning of the nitride. This problem can be addressed by depositing a 0.3-0.5 μm thick layer of TEOS as a hard mask before the photolithography. The TEOS can be etched in the same chamber as the nitride, and is removed during the release etch.

 Figure 3(b) shows a nitride diaphragm after deposition but before patterning. After patterning, thin oxide stubs are exposed. These thin oxide stubs allow etchant to reach the oxide layers underneath the diaphragm during the release etch. The devices are placed in an HF-based, release etch solution. This solution removes the oxide layers beneath the diaphragm, thus freeing the diaphragm. The thin oxide of BPSG etches much faster than the TEOS layer. During the release etch, the BPSG etches rapidly in the lateral direction, exposing the thick oxide. The exposed thick oxide must only be etched through

its thickness and is etched quickly. Hence, the net lateral etch rate of the thin/thick oxide stack approaches that of the BPSG. Figure 3(c) shows a partially-released 100μm-diameter diaphragm. The white material in the center is unetched oxide beneath the nitride diaphragm. A fully-released 50μm diaphragm is shown in Figure 3(d), with the inset showing a close-up of one of the etch ports. The etch port was attacked by the HF during the release etch. This nitride attack can be lessened by using concentrated solutions of HF or concentrated solutions of HF:HCl[6,7]. After the oxide has been etched away in the HF solution, the wafers are dilution rinsed with deionized water, removed from the water, and allowed to air dry.

(a) Deposit low stress nitride.   (b) Deposit and pattern thick oxide.

(c) Deposit and pattern thin oxide.

(d) Deposit and pattern diaphragm nitride.   (e) Etch oxides in HF solution.   (f) Seal diaphragms with nitride.

(g) Deposit, implant, and pattern piezoresistive polysilicon   (h) Deposit and pattern aluminum.

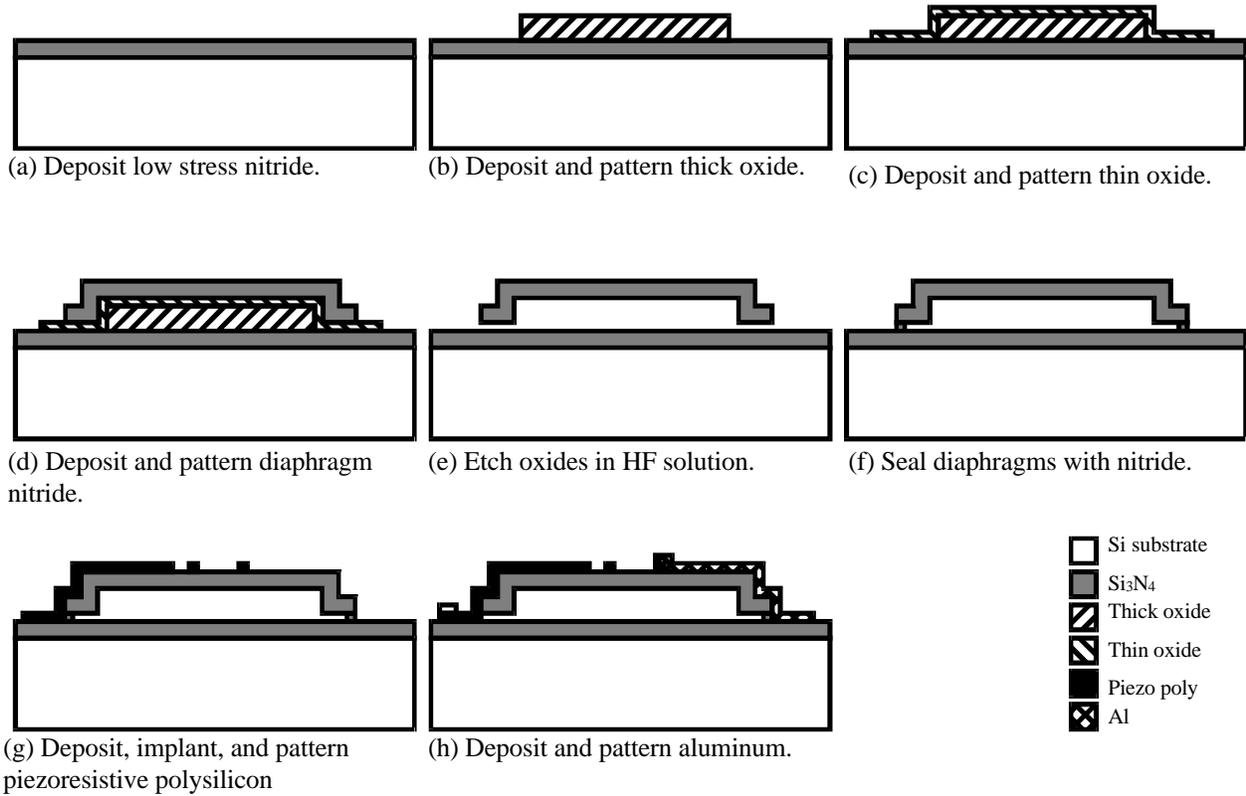|	
☐	Si substrate
▨	Si₃N₄
▨	Thick oxide
▨	Thin oxide
■	Piezo poly
⊠	Al

Figure 2. Schematic fabrication sequence of microsensors.

Following the drying process, the diaphragms are sealed by another low-stress nitride deposition of ~0.1μm, which plugs the etch ports. If the etch ports are significantly enlarged during the HF etch, the sealing layer thickness must be increased. Ideally, without the attack of the nitride during the release etch, the minimum thickness of the sealing layer would be half the thickness of the thin oxide. Deposition conditions during the sealing step are 850°C and 250 mTorr, so the residual pressure at room temperature (assuming ideal gases) should be ≈67 mTorr. Some researchers have reported residual pressures of 200-300 mTorr[8] for nitride sealing. The actual pressure, though stable for any given diaphragm, may be variable and non-repeatable across a substrate and from substrate to substrate. However, since residual pressures are under 1 Torr, the pressure variations are insignificant relative to the sensitivity of the sensors for pressure measurements at or above atmospheric pressure. Other sealing techniques can be used such as polysilicon deposition[8] or reactive oxide sealing[3].

Once the diaphragms have been sealed, special rinsing and drying techniques are no longer needed. Processing continues with the deposition and ion implantation of the polysilicon piezoresistors. An implant of phosphorus at 80 keV with a dose of $3 \cdot 10^{14}/cm^2$ was chosen to maximize the gauge factor of the polysilicon[9]. After implant, the polysilicon is patterned. Finally, aluminum metallization is deposited and patterned, and the devices are ready to be tested. For corrosion resistance, an additional silicon nitride layer can be deposited and patterned as a passivation layer.

Not shown in Figure 2 is a special shunt layer of *in situ* doped polysilicon. This layer ameliorates the step coverage problems of the aluminum over the diaphragm edges. The shunt layer is patterned with the same mask as the aluminum layer, but is placed directly underneath the piezoresistors. In planar regions, current will flow preferentially through the low

resistance path of the aluminum. Where there are breaks in aluminum over steps, current will flow through the poly shunt layer. The shunt layer can be avoided altogether if more planar topologies are used, such as in references 3 and 10. A planar topology is currently under investigation.
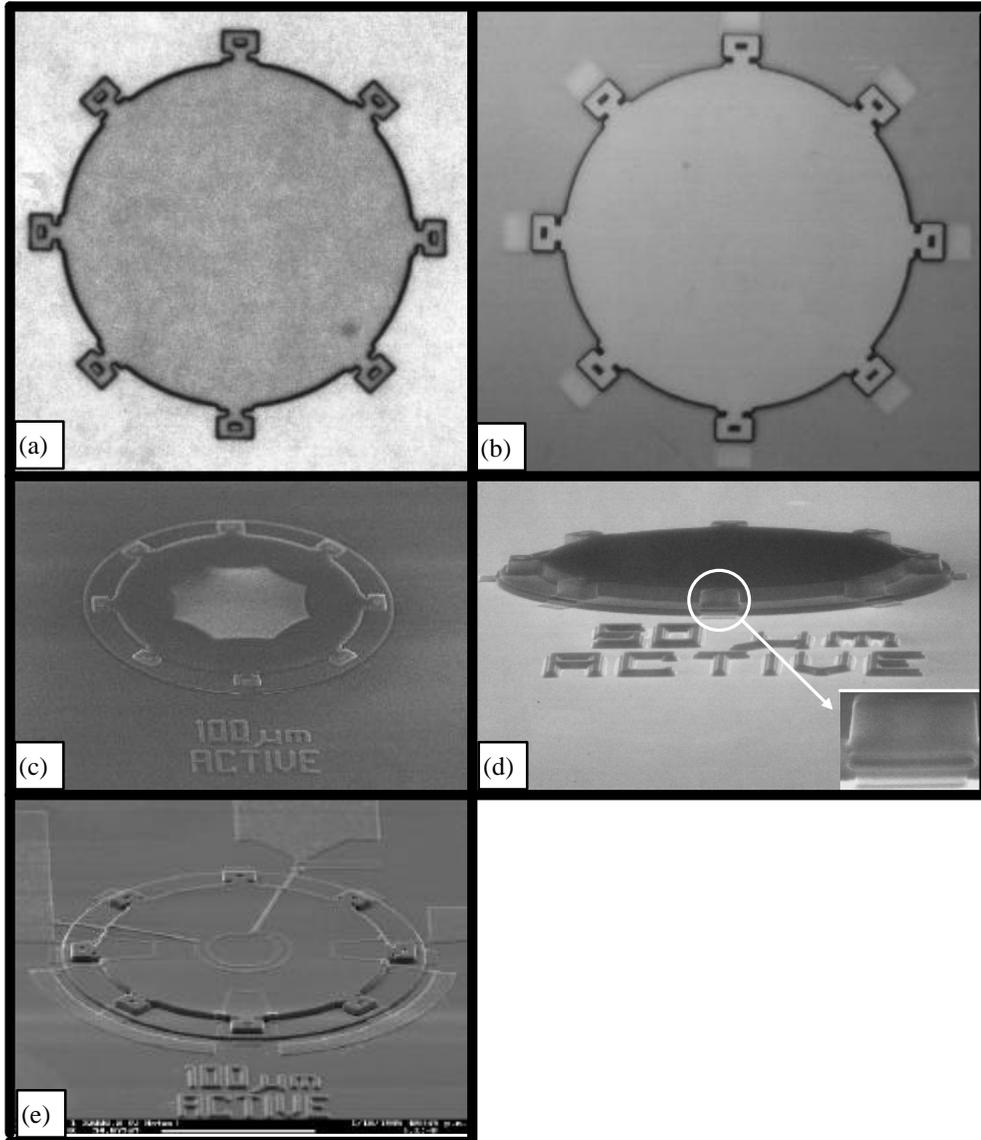

Figure 3. Optical[(a),(b)] and electron[(c)-(e)] micrographs of fabrication sequence. (a) 100μm diameter thick oxide after patterning. (b) 100 μm diameter diaphragm after diaphragm deposition. (c) Partially released 100μm diameter diaphragm. (d) Fully released 50μm diameter diaphragm. (e) Completed 100μm diameter pressure sensor.

2.3   Signal detection

Signal detection is accomplished by connecting the piezoresistive strain gauges in a Wheatstone bridge configuration. This approach has several advantages: 1) first order compensation of common mode signals such as temperature 2) voltage output, which is easily measured, and 3) relative immunity to changes in supply voltage. A current loop detection scheme has similar advantages[11,12], but has slightly more complicated instrumentation requirements.

Three bridge configurations were used: fully active, half active circumferential, and half active radial. These configurations are shown in Figure 4. R_{ac}, R_{dc}, R_{ar} , and R_{dr} are active and dummy circumferential resistors and active and dummy radial resistors, respectively. Active resistors are placed upon diaphragms and are sensitive to applied pressure, whereas dummy resistors are placed either on unreleased diaphragms or in the field region and are insensitive to applied

pressure. Following the schematics of Figure 1 and Figure 4, two diaphragms are required for one fully active bridge sensor,
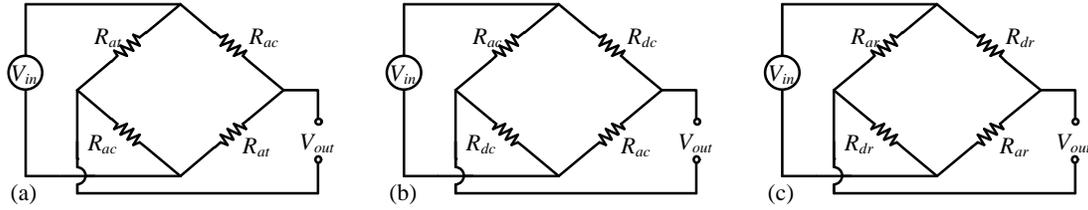

Figure 4. Wheatstone bridge configurations: (a) fully active, (b) half active circumferential, and (c) half active radial.

but four diaphragms (2 active and 2 dummy) are required for one half active bridge sensor. If well matched resistances, R, and resistance changes, ΔR, are assumed then the circumferential and radial resistors will have total resistances under applied pressure of

$$R_{ac} = \Delta R + R, \qquad R_{dc} = R$$
$$R_{ar} = \Delta R - R, \qquad R_{dr} = R$$

(3)

The output voltages become

fully active: $\qquad V_{out} = \left(\dfrac{R+\Delta R}{2R} - \dfrac{R-\Delta R}{2R} \right) V_{in} = \dfrac{\Delta R}{R} V_{in}$

half active circumferential: $V_{out} = \left(\dfrac{R}{2R+\Delta R} - \dfrac{R+\Delta R}{2R+\Delta R} \right) V_{in} = \left(\dfrac{-\Delta R}{2R+\Delta R} \right) V_{in} \approx \dfrac{-\Delta R}{2R} V_{in}$

half active radial: $\qquad V_{out} = \left(\dfrac{R}{2R-\Delta R} - \dfrac{R-\Delta R}{2R-\Delta R} \right) V_{in} = \left(\dfrac{\Delta R}{2R+\Delta R} \right) V_{in} \approx \dfrac{\Delta R}{2R} V_{in}$

(4)

Thus the fully active bridge configuration has twice the sensitivity of the half active bridges to pressure changes and are made with fewer diaphragms. If the resistor layout of Figure 1 was modified to accommodate two separate radial resistors on one diaphragm (and no circumferential resistors), a single diaphragm, half active sensor could be made. The necessary dummy resistors could be placed close to the diaphragm. The resulting sensor would have half the sensitivity, but occupy less area.

3. RESULTS AND DISCUSSION

Several device lots were fabricated and tested in a small pressure chamber constructed of standard stainless steel pipe fittings, shown schematically in Figure 5. A typical response curve is shown in Figure 6. The measurements were performed with compressed air under static conditions using an Omega PX300 commercial pressure sensor with 0.25% full scale accuracy. The response has good linearity below 40 psia, but begins to saturate above that level. The non-linearity could be due to the diaphragm contacting the substrate, which is consistent with the P_{max} of a 0.8 μm thick diaphragm shown in Table 1. However, other factors, such as stress stiffening and imperfectly clamped edges are not taken into account by this analysis.

The data in Figure 6 were fit by the least squares method to a second order polynomial of the form:

$$\frac{V_{out}}{V_{in}} = C_2 P^2 + C_1 P + C_0 \qquad (5)$$

The constants C_2, C_1, and C_0 represent the nonlinearity, sensitivity, and zero pressure offset voltage of the sensor, respectively. These constants were determined for a number of sensors in all three configurations outlined in section **2.3**. The results are shown in the histograms of Figure 7, 8 and 9. Since Equation (5) is normalized by applied voltage, the units of volts appear in the denominator of all constants. The insets of the offset voltage graphs are histograms of the data points near zero offset with finer data bins. The large variation of all constants suggests poor process control during fabrication. Part of this variation is attributed to an improper dopant activation anneal for the piezoresistors. Additionally, the thickness control and sidewall etching issues mentioned earlier may contribute to variance. Non-functional sensors were optically examined to identify the source of failure. Of the samples that were examined, all had cracked or broken diaphragms. Principal causes of diaphragm damage are improper handling and hydrostatic pressures during wet cleans after the diaphragms have been sealed.
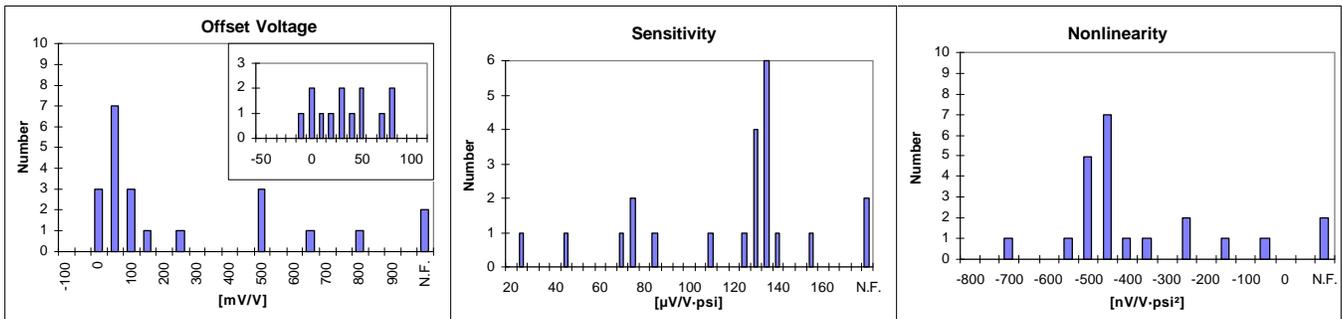

Figure 7. Histograms for full bridge sensors. Applied voltage is 10 V, and there were 22 samples total. N.F.=non functional.
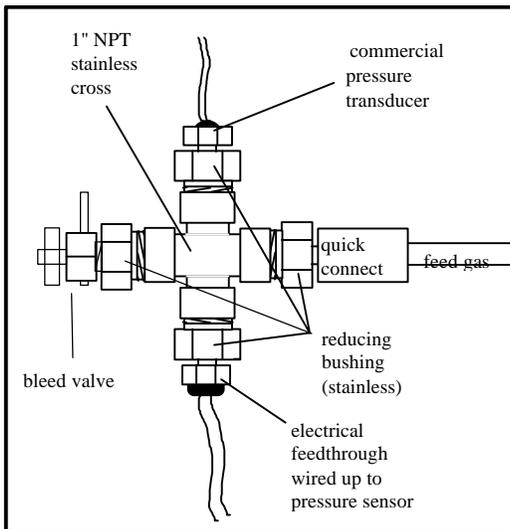

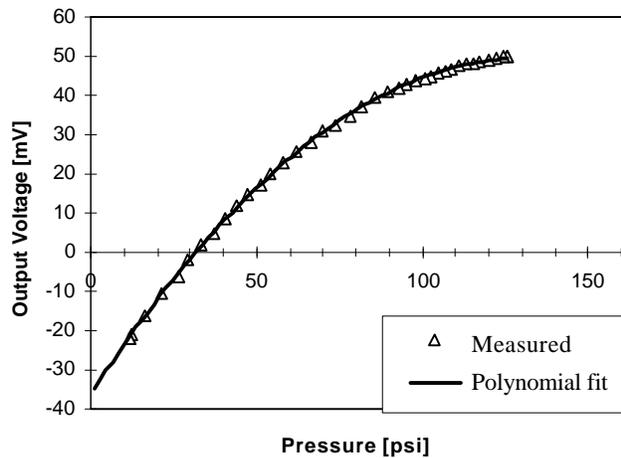Figure 5. Pressure testing apparatus

Figure 6. Typical response curve of fully active bridge sensor. Applied voltage is 10 V.
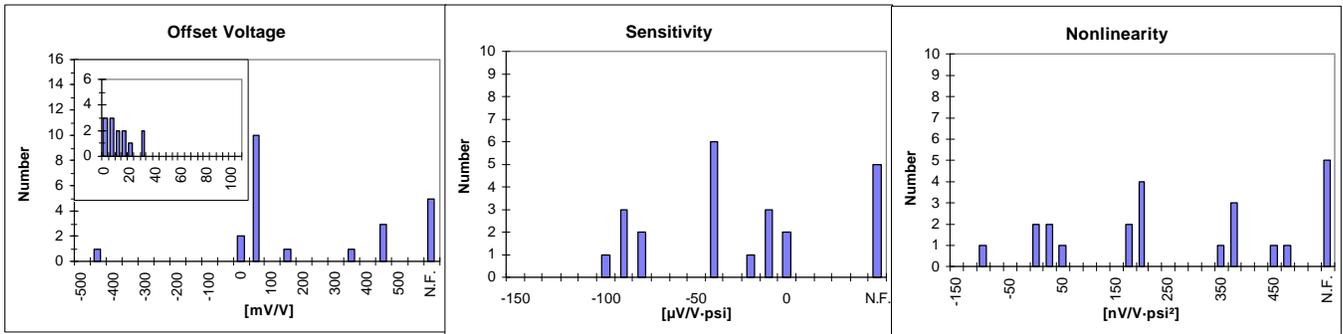
Figure 8. Histograms for half active circumferential bridge sensors. Applied voltage is 10 V, and there were 23 samples total. N.F.=non functional.
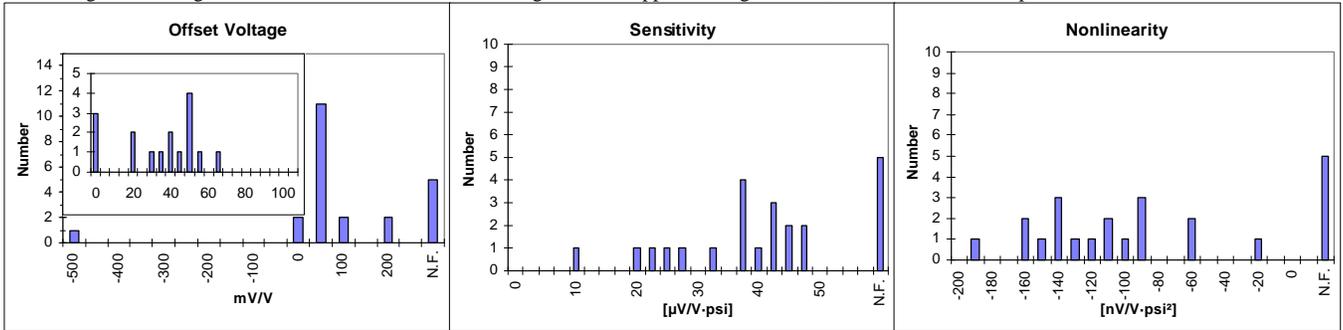

Figure 9. Histograms for half active radial bridge sensors. Applied voltage is 10 V, and there were 23 samples total. N.F.=non functional.

Some of the polysilicon piezoresistors were examined for noise characteristics. Preliminary results of the noise power in Figure 10 suggest that $1/f$ noise dominates.
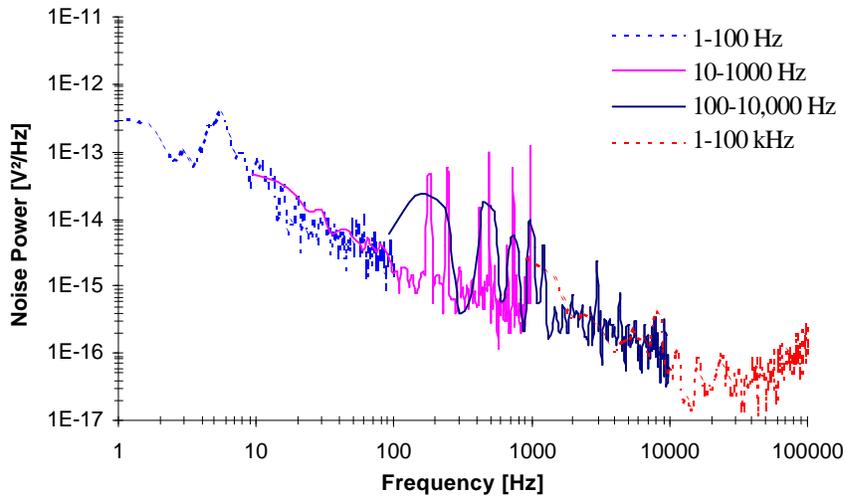

Figure 10. Noise power for a single polysilicon piezoresistor measured over several decades of frequency ranges. Each trace corresponds to the two decade bandwidth indicated.

4. CONCLUSION

A pressure sensor array has been designed and fabricated. The sensors are based on silicon nitride diaphragms with polysilicon piezoresistors. All tools and materials were CMOS compatible. Some design considerations regarding diaphragm geometry and Wheatstone bridge configuration were presented and discussed Several processing issues were addressed, including diaphragm patterning, release etching, and aluminum step coverage. Offset voltage, sensitivity, and

nonlinearity of several sensors were extracted from least squares fits of measured data. Preliminary data indicate that the dominant source of noise is $1/f$ noise.

5. ACKNOWLEDGMENTS

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