

## Ali Pinar

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- CONTACT INFORMATION      Quantitative Modeling & Analysis Dept.      *Voice:* (925) 294 4683  
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Livermore, CA 94551 USA      <http://www.sandia.gov/~apinar/>
- RESEARCH INTERESTS      Modeling and analysis of complex networks, graph algorithms, graph mining, combinatorial algorithms, combinatorial scientific computing, parallel and high performance computing, interconnection networks, scientific computing, computational electric power systems, data analysis.
- EDUCATION      **University of Illinois at Urbana-Champaign (UIUC)**
- Ph.D. in Computer Science, 2001  
with the option of Computational Science and Engineering
    - Thesis title: “Combinatorial Algorithms in Scientific Computing,”
    - Co-advisors: Michael Heath and Bruce Hendrickson
- Bilkent University, Ankara, Turkey**
- M.S. in Computer Engineering and Information Science, 1996
    - Thesis title: “Decomposing Linear Programs for Parallel Solution,”
    - Advisor: Cevdet Aykanat
  - B. S. in Computer Engineering and Information Science, 1994
- HONORS, AWARDS AND RECOGNITION
- Senior member, Institute of Electrical and Electronics Engineers (IEEE) (2012–)
  - Elected *Program Director of SIAM Activity Group on Supercomputing* (Jan 1, 2012–Dec 31, 2013).
  - Senior member, Association of Computing Machinery (ACM) (2011–)
  - Elected *Secretary of SIAM Activity Group on Supercomputing* (Jan 1, 2008–Dec 31, 2009).
  - Research Grant (2009–2012), Principal Investigator  
“Scalable Methods for Representing, Characterizing, and Generating Large Graphs,”  
funded by DOE Office of Science, Applied Mathematics program \$2.1M/3 years
  - Research Grant (2004–2007), Principal Investigator  
“Advanced Computational Tools for Electric Power Systems,”  
funded under the Laboratory Directed Research Program for \$650K/3 years
  - Research Grant (2001–2004), Principal Investigator  
“Combinatorial Algorithms and Scientific Computing,”  
funded under the Laboratory Directed Research Program for \$265K/3 years

- SPOT Awards
  - Sandia National Laboratories, 2009, 2009
  - Lawrence Berkeley National Laboratory 2005
- *John Von Neumann Research Fellowship in Computational Science*, by Sandia National Laboratories, 2001 (declined)
- *Alston S. Householder Fellowship in Scientific Computing*, by Oak Ridge National Laboratory, 2001 (declined)
- *Outstanding Graduate Student Service Award*, Dept. of Computer Science, U. of Illinois at Urbana-Champaign, 2000

PROFESSIONAL  
EXPERIENCE

- Principal Member of the Technical Staff (Oct 2008 – )  
Sandia National Laboratories
- Computer Systems Engineer III, (May, 2004 – Oct 2008)  
Lawrence Berkeley National Laboratory
- Postdoctoral Researcher, (Oct, 2001 – May, 2004)  
Lawrence Berkeley National Laboratory
- Visiting Researcher, (summers 1999, 2000)  
Sandia National Laboratories
- Research Assistant, (Aug, 1997 – Aug, 2001)  
Department of Computer Science  
Computational Science and Engineering Program, UIUC
- Teaching Assistant, (Sep., 1994 – July 1997)  
Dept. of Computer Engineering and Information Science,  
Bilkent University, Ankara, Turkey

TEACHING AND  
SUPERVISING  
EXPERIENCE

- Supervisor for
  - Nurcan Durak, postdoc, 2011– , Louisville U.
  - David Gleich, Von Neumann postdoc, 2010–2011, Stanford U.
  - Vanessa Lopez, postdoc, 2005–2006, UIUC
  - Chris Quinn, Krell fellow, 2012, UIUC
  - Christine Task, summer student, 2012, Purdue University
  - Isabelle Stanton, summer student, 2010, UC Berkeley
  - Matthew Rocklin, summer student, 2010, U. Chicago
  - Adam Reichert, summer student, 2006, UIUC
  - Yonatan Fogel, summer student, 2006, State University of New York at Stony Brook
  - Virginia Vassilevska, summer student, 2003, Carnegie Mellon University
  - Manmeet Singh, summer student, 2003, UIUC
  - Tao Tao, summer student, 2002, UIUC

- Feida Zhu, summer student, 2002, UIUC
- Teaching Assistant for
- Algorithms (for senior students)
  - Program Verification (for junior and senior students)
  - Combinatorics and Graph Theory (for junior students)

JOURNAL  
PUBLICATIONS

1. C. Seshadhri, T. Kolda, and A. Pinar, “[Community structure and scale-free collections of Erdős–Rényi graphs](#)” *Physical Review E*, Vol. 85, No.5, 2012.
2. M. Rocklin and A. Pinar, “[On Clustering of Graphs with Multiple Edge Types](#),” to appear in *Internet Mathematics*.
3. I. Stanton and A. Pinar, “[Constructing and uniform sampling of graphs with prescribed joint degree distribution using Markov Chains](#),” *ACM Journal on Experimental Algorithmics*, Vol. 17, No. 1, 2012.
4. E. Kayaaslan, A. Pinar, U. Catalyurek, and C. Aykanat, “[Partitioning Hypergraphs in Scientific Computing Applications through Vertex Separators on Graphs](#),” *SIAM Journal on Scientific Computing*, Vol. 34, No. 2, pages A970–A992, 2012.
5. C. Janssen, H. Adalsteinsson, S. Cranford, J. Kenny, A. Pinar, D. Evensky, and J. Mayo, “[A Simulator for Large-scale Parallel Computer Architectures](#),” *International Journal of Distributed Systems and Technology*, Vol. 1, No. 2, pages 57–73, 2010.
6. A. Pinar, J. Meza, V. Donde, and B. Lesieutre, “[Optimization Strategies for the Vulnerability Analysis of the Power Grid](#),” *SIAM Journal on Optimization*, Vol. 20, No. 4, pages 1786–1810, 2010.
7. S. Kamil, L. Oliker, A. Pinar, and J. Shalf, “[Communication Requirements and Interconnect Optimization for High-End Scientific Applications](#),” *IEEE Transactions on Parallel and Distributed Computing*, Vol. 21, No. 2, pages 188–202, 2010.
8. A. Pinar, E. Tabak, and C. Aykanat, “[One dimensional partitioning for heterogeneous systems](#),” *Journal of Parallel and Distributed Computing*, Vol. 68, No. 11, pages 1473–1486, 2008.
9. V. Donde, V. Lopez, B. Lesieutre, A. Pinar, C. Yang, and J. Meza, “[Severe Multiple Contingency Screening in Electric Power Systems](#),” *IEEE Transactions on Power Systems*, Vol. 23, No. 2, pages 406–417, 2008.
10. A. Pinar, E. Chow, and A. Pothén, “[Combinatorial Techniques for Constructing Sparse Null-space Bases](#),” *Electronic Transactions on Numerical Analysis, special volume on saddle point problems: numerical solution and applications*, Vol. 22, pages 122–145, 2006.

11. A. Pinar and B. Hendrickson, “[Improving Load Balance with Flexibly Assignable Tasks](#),” *IEEE Transactions on Parallel and Distributed Systems*, Vol. 16, No. 10, pages 956–965, 2005.
12. A. Pinar and V. Vassilevska, “[Finding Nonoverlapping Substructures of a Sparse Matrix](#),” *Electronic Transactions on Numerical Analysis, special volume on combinatorial scientific computing*, Vol. 21, pages 107–124, 2005.
13. A. Pinar and B. Hendrickson, “[Interprocessor Communication with Limited Memory](#),” *IEEE Transactions on Parallel and Distributed Systems*, Vol. 15, No. 7, pages 606–616, 2004.
14. A. Pinar and C. Aykanat, “[Fast Optimal Load Balancing Algorithms for 1D Partitioning](#),” *Journal of Parallel and Distributed Computing*, Vol. 64, No. 8, pages 974–996, 2004.
15. C. Aykanat, A. Pinar, and U. Catalyurek, “[Permuting Sparse Rectangular Matrices into Block-Diagonal Form](#),” *SIAM Journal on Scientific Computing*, Vol. 25, No. 6, pages 1860–1879, 2004.
16. A. Pinar and C.L. Liu, “[Compacting Sequences with Invariant Transition Frequencies](#),” *ACM Transactions on Design Automation of Electronic Systems*, Vol. 8, No. 2, pages 214–221, 2003.

PENDING  
JOURNAL  
PUBLICATIONS

17. C. Seshadhri, A. Pinar, and T. Kolda, “[An In-Depth Analysis of Stochastic Kronecker Graphs](#),” *submitted to Journal of ACM*.

BOOK CHAPTER

18. A. Pinar and B. Hendrickson, “[Combinatorial Parallel and Scientific Computing](#),” chapter in *Parallel Processing for Scientific Computing*, editors: M. Heroux, P. Raghavan, and H. Simon, SIAM, 2006.

REFEREED  
CONFERENCE  
PUBLICATIONS

19. N. Durak, A. Pinar, T. Kolda, C. Seshadhri, “The degree relations of triangles in real-world networks and graph models,” in Proc. CIKM 12.
20. J. Ray, A. Pinar, and C. Seshadhri, “[Are we there yet? When to stop a Markov chain while generating random graphs](#),” to appear in Proc. WAW12.
21. R. Chen, A. Cohn, N. Fan, and A. Pinar, “ [\$N - k - \epsilon\$  Survivable Power System Design](#),” in 12<sup>th</sup> *International Conference on Probabilistic Methods Applied to Power Systems (PMAAPS12)*.
22. A. Pinar, C. Seshadhri, and T. Kolda, “[The Similarity of Stochastic Kronecker Graphs to Edge-Configuration Models](#)”, in *Proc. SIAM Data Mining, 2012*
23. C. Seshadhri, A. Pinar, and T. Kolda, “[An In-Depth Study of Stochastic Kronecker Graphs](#),” *Proc. ICDM 2011*.

24. R. Chen, A. Cohn, and A. Pinar, “[An Implicit Optimization Approach for Survivable Network Design](#),” *Proc. 2011 IEEE 1<sup>st</sup> International Network Science Workshop (NSW 2011)*.
25. M. Rocklin and A. Pinar, “[Latent Clustering on Graphs with Multiple Edge Types](#),” *Proc. 8th Workshop on Algorithms and Models for the Web Graph (WAW11)*.
26. I. Stanton and A. Pinar, “[Constructing and sampling graphs with prescribed joint degree distribution using Markov Chains](#),” *Proc. ALENEX 11*.
27. M. Rocklin and A. Pinar, “[Computing an Aggregate Edge-weight function for Clustering Graphs with Multiple Edge Types](#)”, in *Proc. 7th Workshop on Algorithms and Models for the Web Graph (WAW10)*.
28. B. Lesieutre, A. Pinar, and S. Roy, “[Power System Extreme Event Detection: The Vulnerability Frontier](#),” in *Proc. 41st Hawaii International Conference on System Sciences*, pages 184, Waikoloa, Big Island, HI, 2008.
29. S. Kamil, A. Pinar, D. Gunter, M. Lijewski, L. Oliker, and J. Shalf, “[Reconfigurable hybrid interconnection for static and dynamic scientific applications](#),” *Proc. of the 4th International Conference on Computing Frontiers*, pages 183–195, Ischia, Italy, 2007.
30. A. Pinar, A. Reichert, and B. Lesieutre, “[Computing Criticality of Lines in a Power System](#),” *Proc. 2007 IEEE International Symposium on Circuits and Systems*, pages 65–68, New Orleans, LA, May 2007.
31. B. Lesieutre, S. Roy, V. Donde, and A. Pinar, “[Power system extreme event analysis using graph partitioning](#),” *Proc. 39th North American Power Symposium*, Carbondale, IL, October 2006.
32. A. Pinar, T. Tao, and H. Ferhatosmanoglu, “[Compressing Bitmap Indices by Data Reorganization](#),” *Proc. 21st International Conference on Data Engineering (ICDE05)*, pages 310–321.
33. V. Donde, V. Lopez, B. Lesieutre, A. Pinar, C. Yang, and J. Meza, “[Identification of severe multiple contingencies in electric power networks](#),” *Proc. 38th North American Power Symposium*, Ames, IA, October 2005.
34. A. Pinar and B. Hendrickson, “[Exploiting Flexibly Assignable Work to Improve Load Balance](#),” *Proc. ACM 14th Symp. Parallel Algorithms and Architectures (SPAA) 2002*, pages 155–163.
35. A. Pinar and B. Hendrickson, “[Graph Partitioning for Complex Objectives](#),” *Proc. 15th International Parallel and Distributed Processing Symp. (IPDPS)*, IEEE, 2001.
36. A. Pinar and B. Hendrickson, “[Communication Support for Adaptive Computation](#),” in *Proc. SIAM Conf. on Parallel Processing for Scientific Computing*, 2001.

37. A. Pinar and B. Hendrickson, “[Interprocessor Communication with Memory Constraints](#),” *Proc. ACM Symp. Parallel Algorithms and Architectures (SPAA)* 2000, pages 39–45.
38. L. Fleischer, B. Hendrickson, and A. Pinar, “[On Identifying Strongly Connected Components in Parallel](#),” *Lecture Notes in Computer Science*, Vol. 1586, pages 505–511.
39. A. Pinar and M. Heath, “[Improving Performance of Sparse Matrix-Vector Multiplication](#),” *Proc. Supercomputing 99*, 1999.
40. A. Pinar and C.L. Liu, “[Power Invariant Vector Sequence Compaction](#),” *Proc. 1998 IEEE/ACM International Conf. Computer Aided Design*, pages 473–476, 1998.
41. A. Pinar and C. Aykanat, “[Sparse Matrix Decomposition with Optimal Load Balancing](#),” *Proc. International Conf. High Performance Computing (HiPC) 97*, pages 224–229, 1997.
42. A. Pinar and C. Aykanat, “[An Effective Model to Decompose Linear Programs for Parallel Solution](#),” *Lecture Notes in Computer Science*, Vol. 1184, pages 592–601.
43. A. Pinar, U. Catalyurek, C. Aykanat, and M. Pinar, “[Decomposing Linear Programs for Parallel Solution](#),” *Lecture Notes in Computer Science*, Vol. 1041, pages 473–482.
44. A. Pinar, “A New Genetic Algorithm for Hypergraph Partitioning,” *Proc. Turkish Artificial Intelligence and Neural Networks Symp. (TAINN) 96*, pages 167–176, 1996.
45. A. Pinar and U. Cetintemel, “Wide-Area Distributed Selective Dissemination of Information,” *Proc. Tenth International Symp. on Computer and Information Sciences (ISCIS)*, pages 281–288, 1995.

OTHER  
PUBLICATIONS

46. J. Ray, A. Pinar, and C. Seshadhri, “[A stopping criterion for Markov chains when generating independent random graphs](#),” arXiv:1210.8184.
47. C. Seshadhri, A. Pinar, and T. Kolda, “[Triadic Measures on Graphs: The Power of Wedge Sampling](#),” arXiv:1202.5230, submitted for conference publication.
48. N. Durak, T. Kolda, A. Pinar, and C. Seshadhri, “[A Scalable directed graph model with reciprocal edges](#),” arXiv:1210.5288, submitted for conference publication.
49. E. Otoo, A. Pinar, and D. Rotem, “[A Linear Approximation Algorithm for 2-Dimensional Vector Packing](#),” arXiv:1103.0260.
50. S. Poon, A. Pinar, C. Aragon, and P. Nugent, “Time-Domain Visual Analytics for Astronomical Scheduling,” , Sandia Technical Report 5282050, 2010.

51. E. Otoo, A. Pinar, D. Rotem, and S. C. Tsao, "A File Allocation Strategy for Energy-Efficient Disk Storage Systems," Technical Report: LBNL-637E, Lawrence Berkeley National Laboratory, Berkeley, CA.
52. A. Pinar, Y. Fogel, and B. Lesieutre, "The Inhibiting Bisection Problem," Technical Report: LBNL-62142, Lawrence Berkeley National Laboratory, Berkeley, CA.
53. P. Cesarz, G. Pomann, L. Torre, G. Villarosa, T. Flournoy, A. Pinar, and J. Meza, "Detecting Network Vulnerabilities Through Graph Theoretical Methods," Technical Report: LBNL-63487, Lawrence Berkeley National Laboratory, Berkeley, CA.
54. D. Coppersmith, L. Fleischer, B. Hendrickson, and A. Pinar, "A Divide-and-conquer Algorithm for Identifying Strongly Connected Components in Parallel," IBM Technical Report RC23744, 2005.
55. A. Pinar, "High Performance Combinatorial Algorithms," Technical Report: LBNL- 53989, Lawrence Berkeley National Laboratory, Berkeley, CA.
56. A. Pinar, M. Singh, and E. Ng, "Nested Dissection Orderings for LU Factorization of Unsymmetric Matrices with Static Pivoting," extended abstract in Proc. SIAM Workshop on Combinatorial Scientific Computing.
57. A. Pinar, "Combinatorial Algorithms in Scientific Computing," PhD. Thesis, University of Illinois at Urbana-Champaign, July 2001.
58. A. Pinar, "Decomposing Linear Programs for Parallel Solution," M.S. Thesis, Bilkent University, Ankara, Turkey, July 1996.

PRESENTATIONS  
AND INVITED  
TALKS

1. "The Blocked-Two-Level Erdos-Renyi Graph Model," Current Challenges in Computing Network Science, Napa, CA, August 2012.
2. "The Blocked-Two-Level Erdos-Renyi Graph Model," MMDS 2012. Workshop on Algorithms for Modern Massive Data Sets, Stanford, CA, July 2012.
3. "Fast Counting of Patterns in Graphs," SIAM Annual Meeting, Minneapolis, MN, July 2012.
4. "Blocked-Two-Level Erdos-Renyi Graph Model," 16th Annual Signal & Imaging Sciences Workshop, Livermore, CA, May 2012.
5. "Contingency-Constrained Optimization for Electric Power Systems ," Workshop on Power Grids as Complex Networks: Formulating Problems for Useful Science and Science Based Engineering, Santa Fe Institute, Santa Fe, NM, May 2012.
6. "The Inherent Community Structure in Real-World Graphs," SIAM Conf. Parallel Processing, Savannah, GA, February 2012.

7. "Scalable Methods for Characterizing and Generating Large Graphs," Joint Mathematics Meeting, Boston, MA, January 2012.
8. "Models for Generating Large Realistic Graphs," DOE ASCR applied Math Program PI meeting, Washington DC, October 2011.
9. "Contingency-Constrained Optimization for Electric Power Systems," Scientific Computing series, IBM TJ Watson, Yorktown Heights, NY, August 2011.
10. "Modeling Complex Networks," ICIAM 2011, Vancouver, BC, Canada, July 2011.
11. "Constructing and Sampling Graphs with a Given Joint Degree Distribution," SIAM Workshop on Combinatorial Scientific Computing, Darmstadt Germany, May 2011.
12. "SST/macro: The Structural Simulation Toolkit macroscale components for coarse-grained architecture simulation," SIAM Workshop on Combinatorial Scientific Computing, Darmstadt Germany, May 2011.
13. "Contingency-constrained Power System Planning," SIAM Conf. on Optimization, Darmstadt Germany, May 2011.
14. "A Hitchhiker's guide to Choosing Parameters of Stochastic Kronecker Graphs," SIAM Conf. on Computational Science and Engineering, Reno, NV, February, 2011.
15. "SST/macro: The Structural Simulation Toolkit macroscale components for coarse-grained architecture simulation," SIAM Conf. on Computational Science and Engineering, Reno, NV, February, 2011.
16. "Security-constrained Optimization for Electric Power Systems," INFORMS Computing Society Conference (ICS2011), Monterey, CA, January, 2011.
17. "Computing an Aggregate Edge-weight function for Clustering Graphs with Multiple Edge Types," *7th Workshop on Algorithms and Models for the Web Graph (WAW10)*, Stanford, CA, December, 2010.
18. "Scalable Methods for Representing, Characterizing, and Generating Large Graphs," SIAM Annual Meeting, Pittsburgh, PA, July, 2010.
19. "Networks in Modeling and Simulation," SIAM Conf. on Parallel Processing for Scientific Computing, Seattle, WA, February, 2010.
20. "A Simulator for Large-scale Parallel Computer Architectures," poster presentation, SIAM Conf. on Parallel Processing for Scientific Computing, Seattle, WA, February, 2010.
21. "Scalable Interconnects for Scientific Workloads" SIAM Conf. on Computational Science and Engineering, Miami, FL, March 2009.
22. "Scalable Reconfigurable Interconnects," Workshop on Combinatorial Scientific Computing & Petascale Simulations (CSCAPES), Santa Fe, NM, June 2008.

23. "Combinatorial methods in scientific computing," Center for Advanced Computational Research Seminar, California Institute of Technology, May 2008
24. "Combinatorial Scientific Computing," Department Colloquium, Computer Science and Engineering, UC Riverside, March, 2008.
25. "Vulnerability Analysis of the Power Grid," Mathematical Sciences Research Institute Seminar, Berkeley, CA, June 2007.
26. "Vulnerability Analysis of the Power Grid," Georgia Institute of Technology High Performance Computing Seminar, Atlanta, GA, February 2007.
27. "Vulnerability Analysis of the Power Grid," SIAM Conf. on Computational Science and Engineering, Mesa Verde, CA, February 2007.
28. "Advanced Computational Tools for Electric Power Systems," SIAM Annual Meeting, Boston, MA, July 2006.
29. "Improving Performance of Bitmap Indexing," SIAM Workshop on Combinatorial Scientific Computing, Toulouse, France, June 2005
30. "Alternative Models for Load Balancing," SIAM Conf. on Parallel Processing for Scientific Computing 2004, San Francisco, California, February, 2004.
31. "Nested Dissection Orderings for LU Factorization with Static Pivoting," SIAM Workshop on Combinatorial Scientific Computing, San Francisco, California, February, 2004.
32. "Combinatorial Techniques for Constructing Sparse Null-space Bases," SIAM Conf. on Applied Linear Algebra, Williamsburg, VA, July 2003.
33. "The Nice Basis Problem," Bay Area Scientific Computing Day, Stanford University, March, 2003.
34. "The Nice Basis Problem," Mathematics and Computer Science Division Seminar, Argonne National Laboratory, May, 2003.
35. "Exploiting Flexibly Assignable Work to Improve Load Balance," SIAM 50th Anniversary and 2002 Annual Meeting, Philadelphia, Pennsylvania, July, 2002.
36. "Partitioning for Complex Objectives," International Parallel and Distributed Processing Symp., San Francisco, California, April, 2001.
37. "Combinatorial Algorithms for Adaptive Computation," NERSC Scientific Computing Seminar, Berkeley, California, April, 2001.
38. "Communication Support for Adaptive Computation," SIAM Conf. on Parallel Processing for Scientific Computing 2001, Portsmouth, Virginia, March, 2001.
39. "Interprocessor Communication with Memory Constraints," ACM Symp. on Parallel Algorithms and Architectures (SPAA), Bar Harbor, Maine, July, 2000.
40. "On Identifying Strongly Connected Components in Parallel," International Parallel and Distributed Processing Symp. (IPDPS), Cancun, Mexico, May, 2000.

41. "Improving Performance of Matrix-Vector Multiplication," Supercomputing 99, Portland, Oregon, November, 1999.
42. "Power Invariant Vector Sequence Compaction," International Conf. on Computer Aided Design, San Jose, California, November, 1998.
43. "An Effective Graph Model to Decompose Linear Programs for Parallel Solution," PARA96, Workshop on Applied Parallel Computing in Industrial Problems and Optimization, Lyngby, Denmark, August, 1996.
44. "A New Genetic Algorithm for Hypergraph Partitioning," Artificial Intelligence and Neural Network Symp., Istanbul, Turkey, June, 1996.
45. "Wide-Area Distributed Selective Dissemination of Information," International Symp. on Computer and Information Systems, Izmir, Turkey, November, 1995.
46. "Decomposing Linear Programs for Parallel Solution," Bilkent University, Dept. of Computer Science Seminar, Ankara, Turkey, December, 1995

PROFESSIONAL  
SOCIETY  
MEMBERSHIPS

- Society of Industrial and Applied Mathematics (SIAM), and its activity groups
  - Supercomputing
  - Optimization
  - Computational Science and Engineering
  - Data Mining and Analytics
- Association of Computing Machinery (ACM), Senior member
  - SIG on Knowledge Discovery and Data Mining (SIGKDD)
  - SIG on High Performance Computing (SIGHPC)
- Institute of Electrical and Electronics Engineers (IEEE), Senior member
  - Computer Society
  - Power Systems Society
- INFORMS Computing Society

SERVICES AND  
MEMBERSHIPS

- Associate editor, Journal of Complex Networks, Oxford University Press.
- Editor, special issue of the SIAM Journal on Scientific Computing for CSE13
- Co-chair,
  - SIAM Workshop on Network Science, San Diego, CA, July, 2013.
  - SIAM Conference on Parallel Processing for Scientific Computing, February 2014.
  - SIAM Workshop on Combinatorial Scientific Computing, Darmstadt, Germany, May 2011
- Program or Organizing Committee Member,

- 5th International Workshop on Network Science for Communication Networks (NetSciCom), Turin Italy, 2013.
- SIAM International Conference on Data Mining, Austin, TX, May 2013.
- 27th IEEE International Parallel and Distributed Processing Symposium, Boston Massachusetts, USA, May 2013.
- SIAM International Conference on Data Mining, Anaheim, CA, April 2012.
- DOE O. Science ASCR Applied Math PI Meeting, Washington DC, October, 2011.
- 31st International Conference on Distributed Computing Systems (ICDCS 2011), Minneapolis, Minnesota, June 2011.
- SIAM Conference on Parallel Processing for Scientific Computing, Seattle, Washington, February, 2010.
- 24th International Symposium on Computer and Information Sciences (ISCIS 2009), Northern Cyprus.
- 4th SIAM Workshop on Combinatorial Scientific Computing, Monterey, CA, October 2009.
- 22nd IEEE International Parallel and Distributed Processing Symposium, Miami, Florida, USA, April 2008.
- 2nd International Conference on Grid and Pervasive Computing, Paris, France, May 2007.
- 35th Annual International Conference in Parallel Processing, Columbus Ohio, August 2006.
- 1st International Conference on Grid and Pervasive Computing, Taichung, Taiwan, May 2006.
- 12th Annual International Conference on High Performance Computing (HiPC), Goa, India, 2005.
- 19th International Symposium on Computer and Information Sciences (ISCIS), Antalya, Turkey, 2004.
- Minisymposium Organizer,
  - “Analysis and Modeling of Static and Dynamic Networks,” SIAM Conference on Computational Science and Engineering, Boston, MA, February, 2013
  - “Combinatorial Scientific Computing: Enabling Computational Science and Engineering through Combinatorial Algorithms,” International Conference on Industrial and Applied Mathematics, Vancouver, Canada, July 2011.
  - “Optimization in Electric Power Systems, SIAM Conference on Computational Science and Engineering,” Reno, NV, February, 2011.
  - “Combinatorial Methods in Applications of CSE,” SIAM Conference on Computational Science and Engineering,” Reno, NV, February, 2011.
  - “Spectral Theory and Graphs,” SIAM Conference on Computational Science and Engineering, Reno, NV, February, 2011.
  - “Mathematics of Complex Distributed Interconnected Systems,” SIAM Annual Meeting, Pittsburgh, PA, July 2010.
  - “Combinatorial Scientific Computing,” SIAM Annual Meeting,” Denver, CO, July 2009.
  - “Optimization with Discrete and Continuous Variables,” SIAM Conference on Computational Science and Engineering, Mesa Verde, California, February, 2007.
  - “Computational Challenges in Electric Power Systems,” SIAM Conference on

Parallel Processing for Scientific Computing, San Francisco, California, February, 2006.

- “Parallel Graph Algorithms,” SIAM Conference on Parallel Processing for Scientific Computing, San Francisco, California, February, 2006.
  - “Computational Challenges in Electric Power Systems,” SIAM Conference on Computational Science and Engineering, Orlando, Florida, February, 2005.
  - “Combinatorial Algorithms and Parallel Computing,” SIAM Conference on Parallel Processing for Scientific Computing, San Francisco, California, February, 2004.
  - “Combinatorial Algorithms in Scientific Computing,” SIAM Conference on Computational Science and Engineering, San Diego, California, February, 2003.
- Member, IEEE Computer Society Membership Development Subcommittee
  - Review Panel Member
    - Programs for DOE Office of Science Advanced Scientific Computing Research, 2009 , 2010.
    - NSF Information & Intelligent Systems Division, 2010
  - Volunteer mentor for MentorNet (<http://www.mentornet.net/>)
  - Reviewer, SIAM Journal on Scientific Computing, SIAM Journal on Optimization, SIAM Journal on Discrete Mathematics, SIAM Review, SIAM Matrix Analysis, SIAM Journal on Control and Optimization, IEEE T. Parallel and Distributed Systems, Mathematical Programming, ACM T. on Mathematical Software, Mathematical and Computer Modeling, Journal of Parallel and Distributed Computing, IEEE T. Power Systems, Numerical Linear Algebra with Applications, Electronic Transactions on Numerical Analysis, Parallel Algorithms and Applications, Parallel Computing, Knowledge and Information Systems, International Journal of High Performance Computing, and many conferences.
  - Member, Graduate Study Committee, Dept. of Computer Science, UIUC, 2000-2001.
  - Member, Fellowships, Assistantships, and Admissions Committee, Dept. of Computer Science, UIUC, 1999–2000.
  - Elected President of Faculty of Engineering Student Board, Bilkent University, Turkey (1992–1993).
  - Elected Secretary of Faculty of Engineering Student Board, Bilkent University, Turkey (1991–1992).
  - Secretary, IEEE Bilkent Student Branch, Bilkent University, Turkey (1991-1992).
  - Founding member, Alumni Association of Istanbul High School of Sciences.