

Sandia National Laboratories Multi-Project Wafer Program

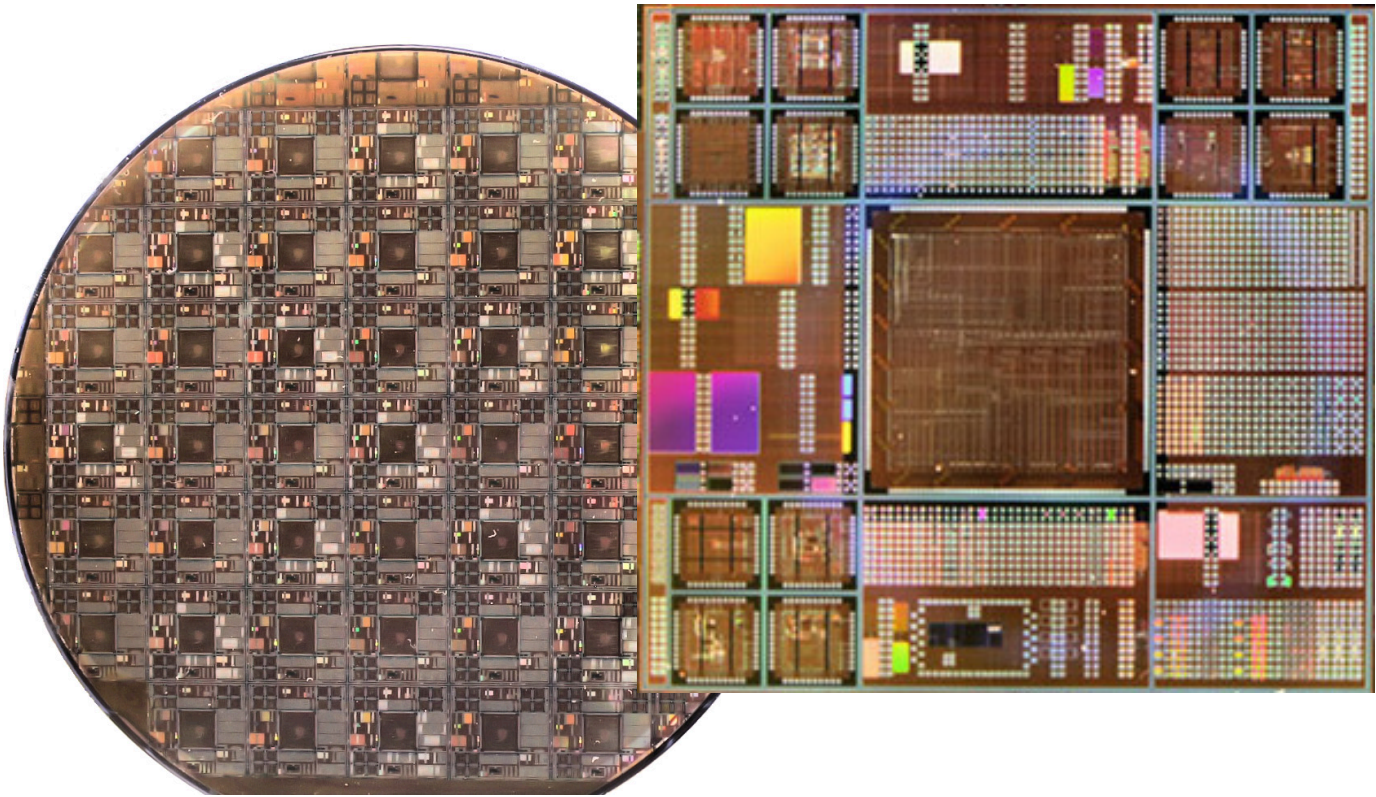
Sandia National Laboratories' Multi-Project Wafer (MPW) Program provides MPW services for CMOS7 radiation-hardened Application-Specific Integrated Circuits (ASICs).

Multi-Project Wafer (MPW) services integrate a number of different integrated circuit designs on a single reticle set. Sharing mask and wafer resources reduces the overall cost per design and makes it more cost effective to produce integrated circuits in low quantities.

Sandia's Multi-Project Wafer Program provides access to its Trusted Design Center and ISO 9001:2000 certified, high-reliability ASIC production facility. The program includes engineering support for MPW design, technology, fabrication, test and packaging.

Program features include:

- Strategic radiation-hardened, 3.3 v, 0.35 μm , mixed-signal, CMOS SOI technology.
- Resistors, MIM capacitors, and diode passives.
- Standard Cells and IOs.
- Library of common digital and analog elements.
- Cadence Process-Design Kit.



For more information email snlasic@sandia.gov
www.sandia.gov/mstc

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